



This diagram enables you to exercise the Exclusive-OR gate
 There are two ways of looking at it:

1. The output is ON if the number of ON inputs is ODD and OFF if the number of ON inputs is even.
2. We can view input 0 as a signal and input 1 as a control.
 If Control is FALSE the signal at the output is the same as the input.
 If Control is TRUE the signal is inverted as it passes through the gate.
 This means that we have a conditional inverter.

Right-click on any of the wires and see how we have made the signal names show on the diagram.

Exercise for the student: invert any one of the XOR gate pins and observe that the action of the gate is reversed.